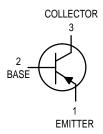
Chopper Transistor PNP Silicon

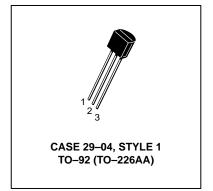


MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	VCEO	-35	Vdc
Collector-Base Voltage	V _{CBO}	-40	Vdc
Emitter-Base Voltage	VEBO	-25	Vdc
Collector Current — Continuous	IC	-150	mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	PD	625 5.0	mW mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C	PD	1.5 12	Watts mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{Stg}	-55 to +150	°C

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Motorola Preferred Device



THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Ambient	R _{0JA} (1)	200	°C/W
Thermal Resistance, Junction to Case	R_{θ} JC	83.3	°C/W

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector-Emitter Breakdown Voltage ⁽²⁾ $(I_C = -10 \text{ mAdc}, I_B = 0)$	V(BR)CEO	-35	_	Vdc
Collector-Base Breakdown Voltage (I _C = -10 μAdc, I _E = 0)	V(BR)CBO	-40	_	Vdc
Emitter-Base Breakdown Voltage ($I_E = -10 \mu Adc$, $I_C = 0$)	V(BR)EBO	-25	_	Vdc
Collector Cutoff Current (V _{CB} = -10 Vdc, I _E = 0)	ICBO	_	-100	nAdc
Emitter Cutoff Current (VBE = -10 Vdc, IC = 0)	IEBO	_	-100	nAdc

2. Pulse Test: Pulse Width \leq 300 $\mu s,$ Duty Cycle \leq 2.0%.

Preferred devices are Motorola recommended choices for future use and best overall value.



ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted) (Continued)

Characteristic	Symbol	Min	Max	Unit
ON CHARACTERISTICS	<u>'</u>		•	
DC Current Gain ($I_C = -12 \text{ mAdc}$, $V_{CE} = -0.15 \text{ Vdc}$)	hFE	30	400	_
Collector-Emitter Saturation Voltage (I _C = -12 mAdc, I _B = -0.4 mAdc) (I _C = -24 mAdc, I _B = -1.0 mAdc)	VCE(sat)	_ _	-0.15 -0.2	Vdc
Base-Emitter Saturation Voltage (I _C = -12 mAdc, I _B = -0.4 mAdc) (I _C = -24 mAdc, I _B = -1.0 mAdc)	V _{BE} (sat)	_ _	-0.85 -1.0	Vdc
SMALL-SIGNAL CHARACTERISTICS				
Common–Base Cutoff Frequency $(I_C = -1.0 \text{ mAdc}, V_{CB} = 6.0 \text{ Vdc})$	fob	4.0	_	MHz
Output Capacitance (V _{CB} = -6.0 Vdc, I _E = 0, f = 1.0 MHz)	C _{obo}	_	20	pF

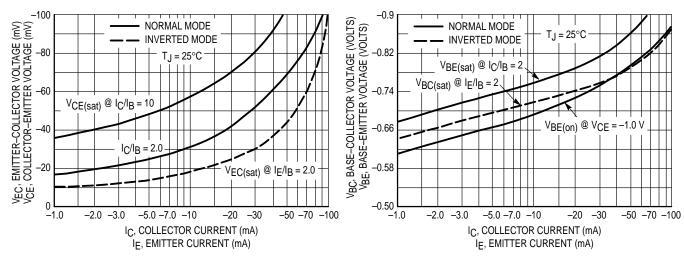


Figure 1. Collector-Emitter Voltage

Figure 2. Base "On" Voltage

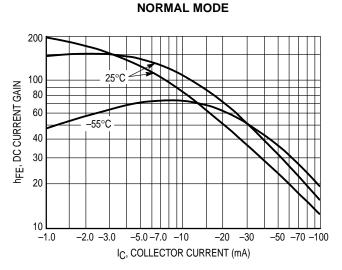


Figure 3. DC Current Gain @ $V_{CE} = -0.15 \text{ Vdc}$

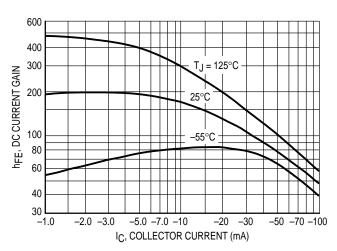


Figure 5. DC Current Gain @ $V_{CE} = -1.0 \text{ Vdc}$

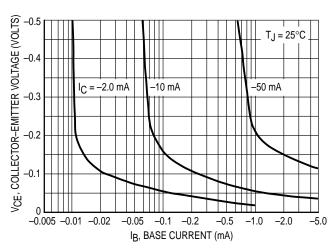


Figure 7. Collector Saturation Region

INVERTED MODE

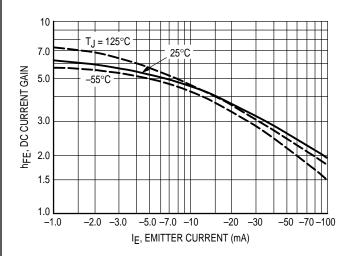


Figure 4. DC Current Gain @ $V_{EC} = -0.15 \text{ Vdc}$

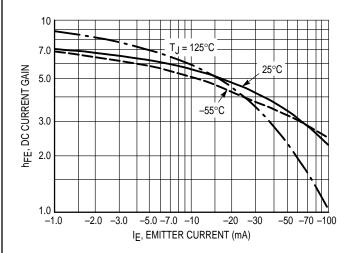


Figure 6. DC Current Gain @ V_{EC} = −1.0 Vdc

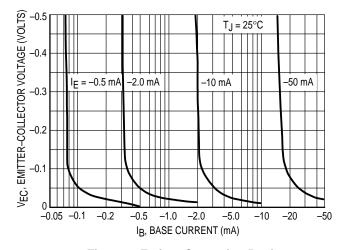


Figure 8. Emitter Saturation Region

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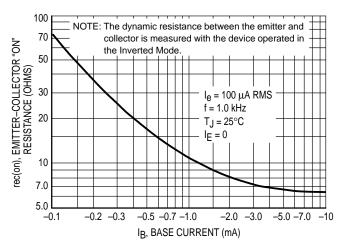


Figure 9. Emitter-Collector "On" Resistance

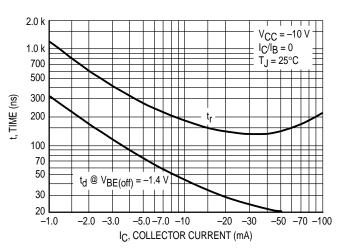
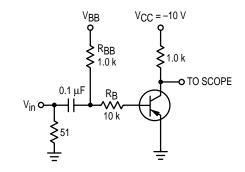


Figure 11. Turn-On Time



	V _{in} (Volts)	V _{BB} (Volts)
ton, td and tr	-12	+1.4
toff, ts and tf	+20.6	-11.6

Voltages and resistor values shown are for $I_C = 10$ mA. $I_C/I_B = 10$ and $I_{B1} = I_{B2}$. Resistor values changed to obtain curves in Figures 11 and 12.

Figure 13. Switching Time Test Circuit

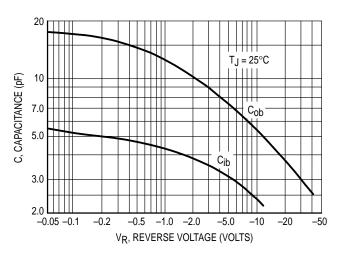


Figure 10. Capacitance

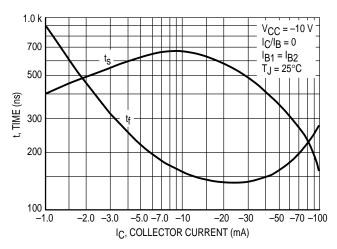


Figure 12. Turn-Off Time

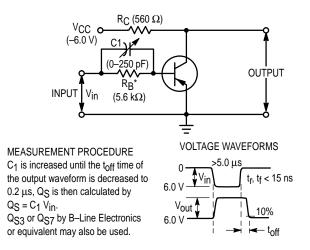
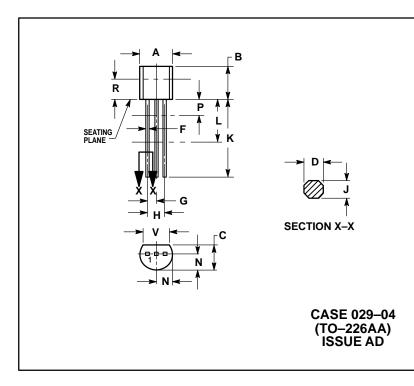


Figure 14. Stored Base Charge Test Circuit

PACKAGE DIMENSIONS



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
 4. DIMENSION F APPLIES BETWEEN P AND L. DIMENSION D AND J APPLY BETWEEN L AND K MINIMUM. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
C	0.125	0.165	3.18	4.19
D	0.016	0.022	0.41	0.55
F	0.016	0.019	0.41	0.48
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
Р		0.100		2.54
R	0.115		2.93	
ν	0.135		3 43	

STYLE 1: PIN 1. EMITTER

2. BASE 3. COLLECTOR

MPS404A

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