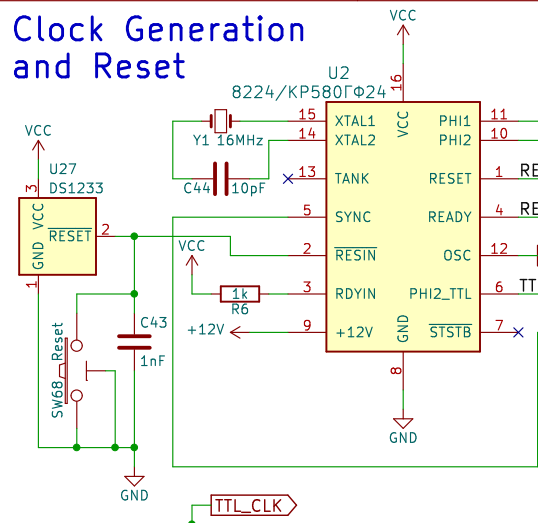
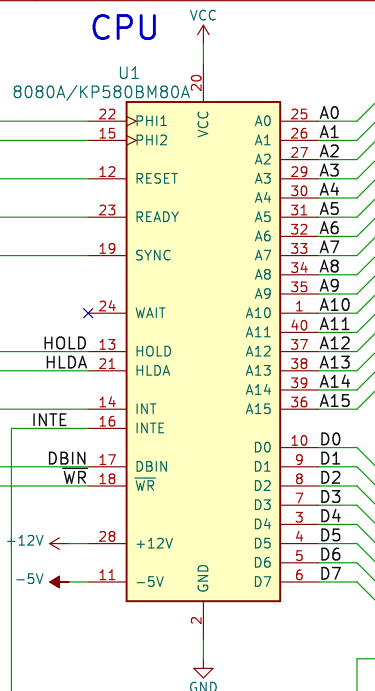


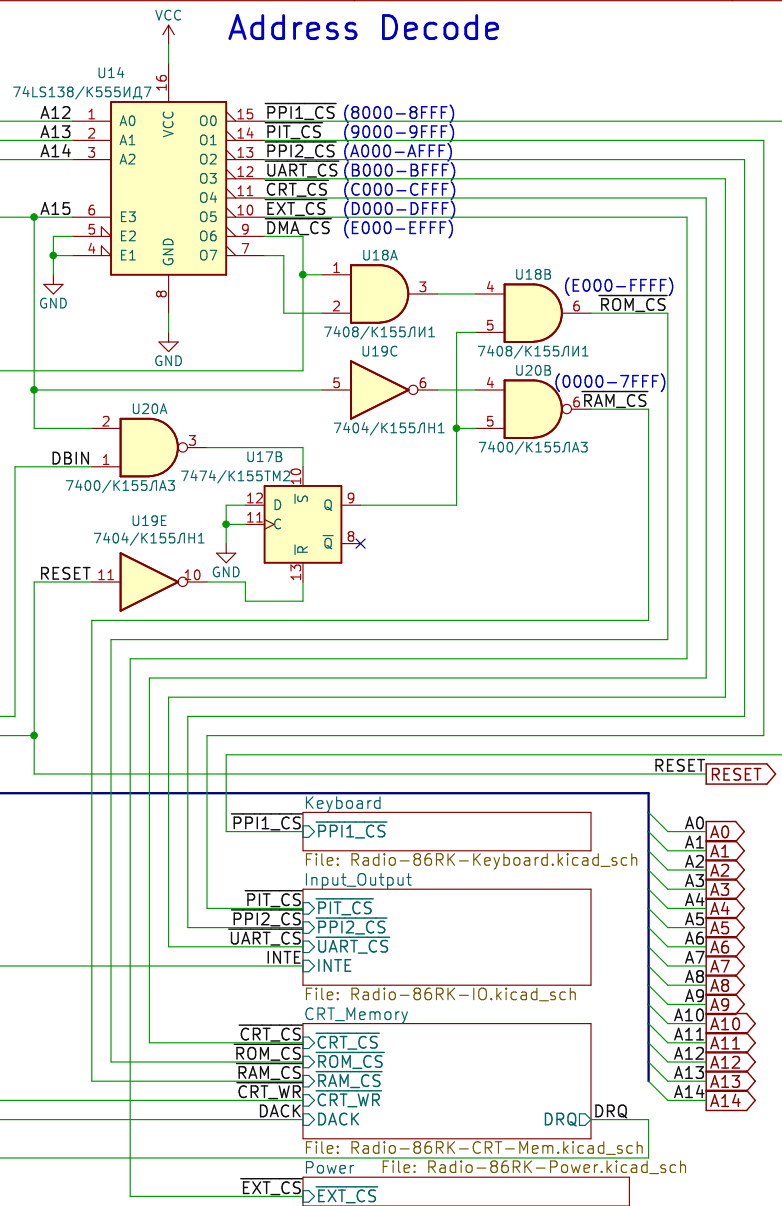
Clock Generation and Reset



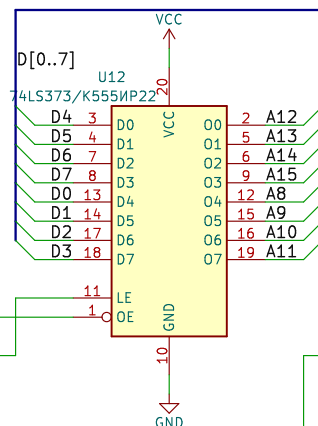
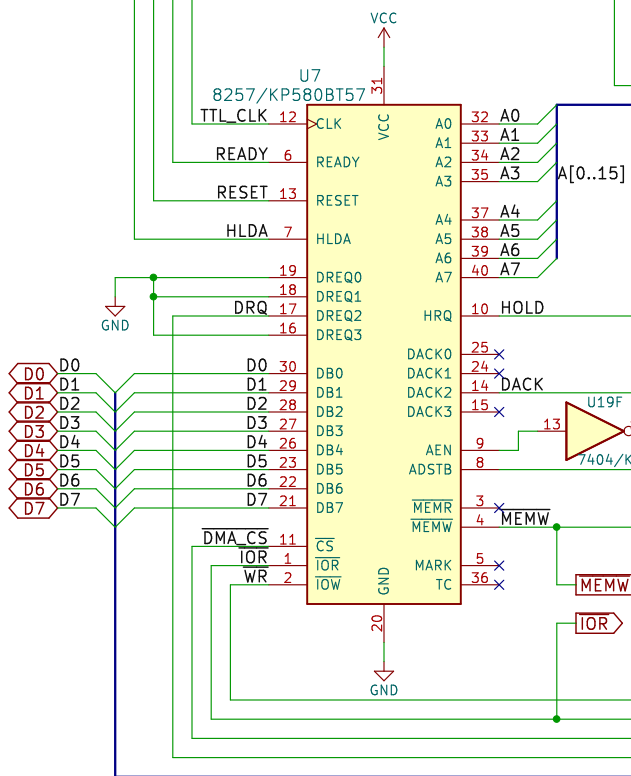
CPU



Address Decode



DMA Controller

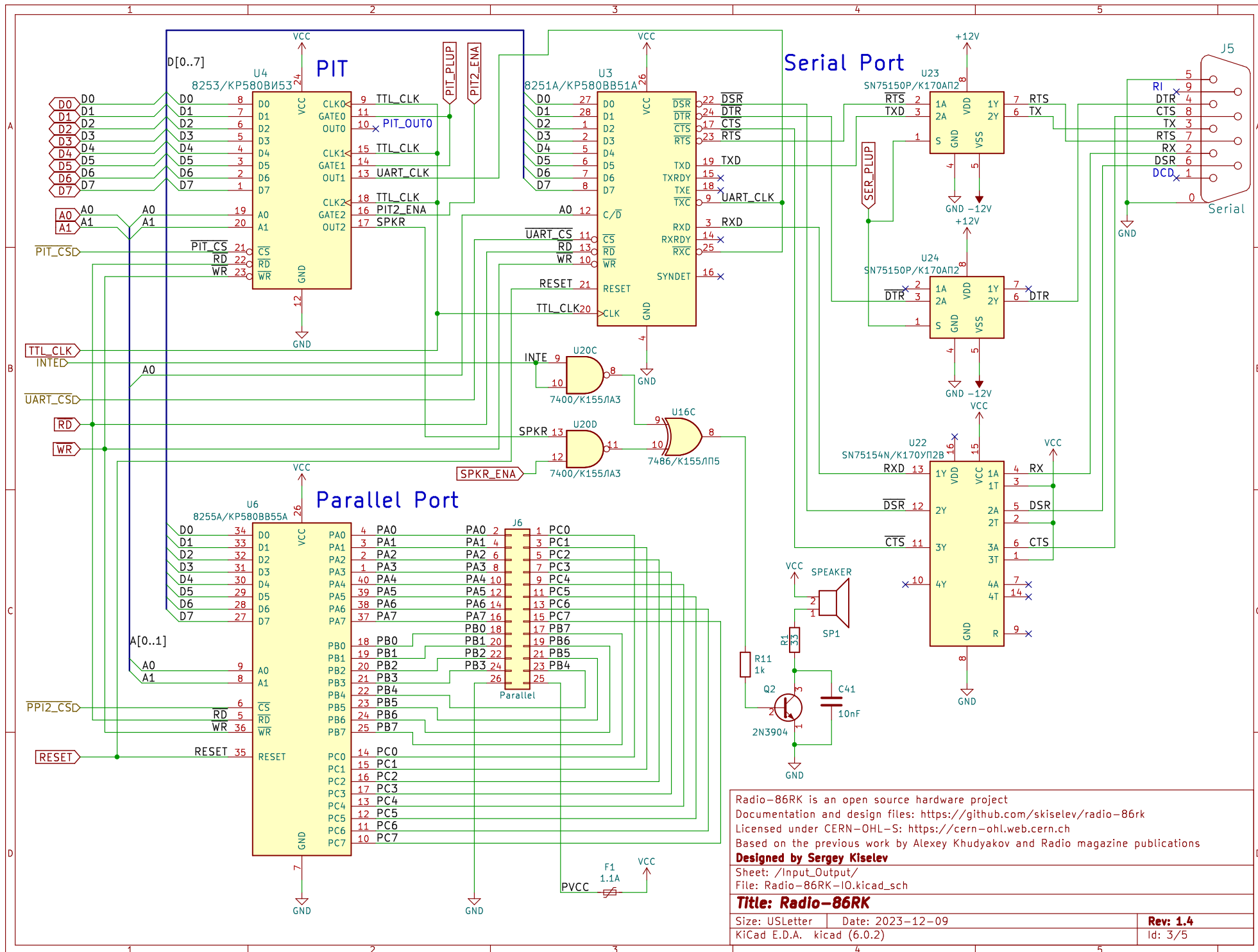


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 Based on the previous work by Alexey Khudyakov and Radio magazine publications
Designed by Sergey Kiselev

Sheet: /
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Title: Radio-86RK

Size: USLetter	Date: 2023-12-09	Rev: 1.4
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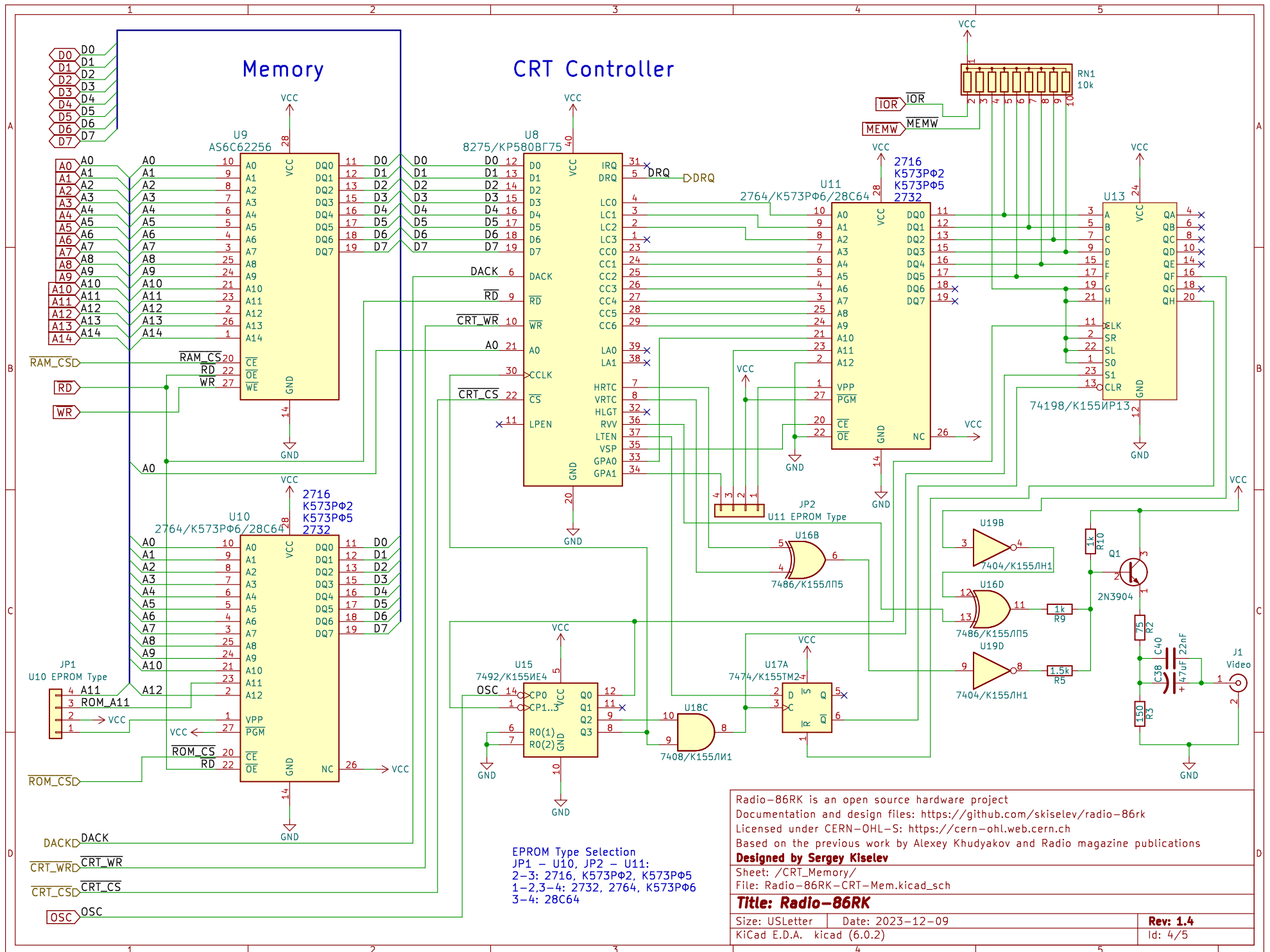
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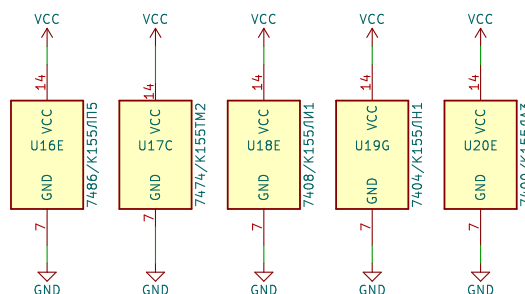
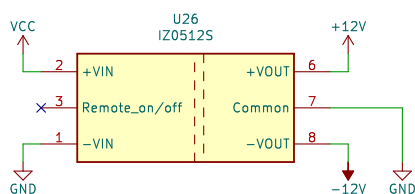
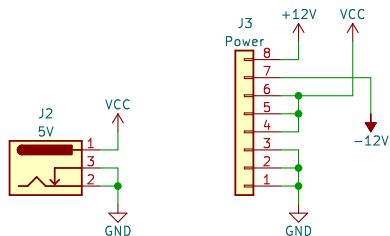
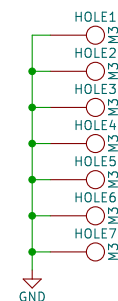
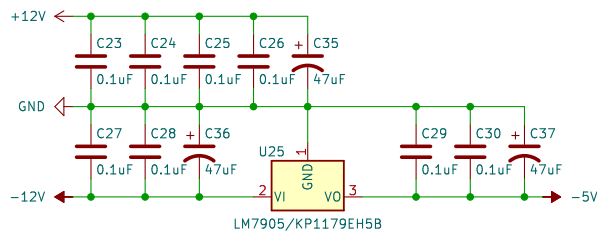
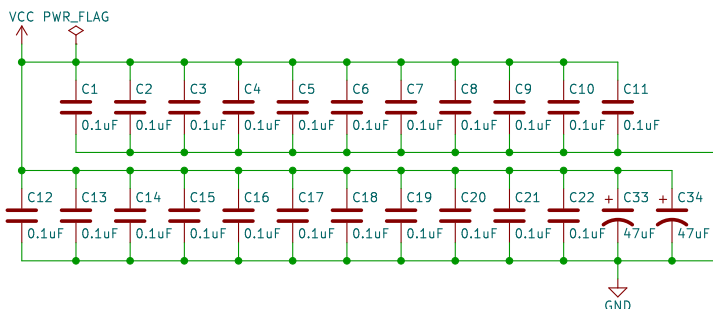
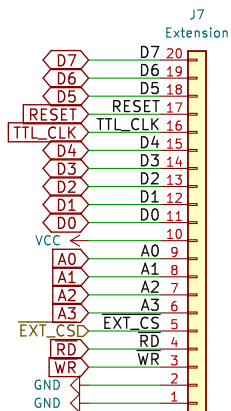
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Size: USLetter Date: 2023-12-09
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Power Supply and Extension Connector



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Title: Radio-86RK
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