## **BF420, BF422**

# **High Voltage Transistors**

### **NPN Silicon**

#### **Features**

• Pb-Free Package is Available\*

#### **MAXIMUM RATINGS**

Rating	Symbol	BF420	BF422	Unit	
Collector - Emitter Voltage	V <sub>CEO</sub>	300 250		Vdc	
Collector - Base Voltage	V <sub>CBO</sub>	300 250		Vdc	
Emitter-Base Voltage	V <sub>EBO</sub>	5.0		Vdc	
Collector Current – Continuous	I <sub>C</sub>	50		mAdc	
Collector Current – Peak	I <sub>CM</sub>	100		mA	
Total Device Dissipation (Note 1)  @ T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub>	830 6.6		mW mW/°C	
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150		°C	

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction–to–Ambient	$R_{\theta JA}$	150	°C/W
Thermal Resistance, Junction-to-Lead	$R_{ heta JL}$	68	°C/W

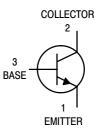
Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

 Mounted on a FR4 board with 200 mm<sup>2</sup> of 1 oz copper and lead length of 5 mm



### ON Semiconductor®

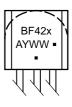
#### http://onsemi.com



#### MARKING DIAGRAM



TO-92 CASE 29 STYLE 14



BF42x = Device Code

x = 0 or 2

A = Assembly Location Y = Year WW = Work Week

(Note: Microdot may be in either location)

= Pb-Free Package

#### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
BF420ZL1	TO-92	2000/Ammo Box
BF420ZL1G	TO-92 (Pb-Free)	2000/Ammo Box
BF422	TO-92	5000 Units/Box
BF422G	TO-92 (Pb-Free)	5000 Units/Box
BF422RL1	TO-92	2000/Tape & Reel
BF422RL1G	TO-92 (Pb-Free)	2000/Tape & Reel
BF422ZL1	TO-92	2000/Ammo Pack
BF422ZL1G	TO-92 (Pb-Free)	2000/Ammo Pack

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

<sup>\*</sup>For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

## BF420, BF422

### **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit
OFF CHARACTERISTICS			1		· ·
Collector – Emitter Breakdown Voltage (Note 1) $(I_C = 1.0 \text{ mAdc}, I_B = 0)$	BF420 BF422	V <sub>(BR)CEO</sub>	300 250	_ _	Vdc
Collector – Base Breakdown Voltage ( $I_C = 100 \mu Adc, I_E = 0$ )	BF420 BF422	V <sub>(BR)CBO</sub>	300 250	- -	Vdc
Emitter – Base Breakdown Voltage ( $I_E = 100 \mu Adc, I_C = 0$ )	BF420 BF422	V <sub>(BR)EBO</sub>	5.0 5.0	- -	Vdc
Collector Cutoff Current (V <sub>CB</sub> = 200 Vdc, I <sub>E</sub> = 0)	BF420 BF422	І <sub>СВО</sub>		0.01 -	μAdc
Emitter Cutoff Current (V <sub>EB</sub> = 5.0 Vdc, I <sub>C</sub> = 0)	BF420 BF422	I <sub>EBO</sub>	_ _	100 -	nAdc
ON CHARACTERISTICS					-
DC Current Gain ( $I_C = 25 \text{ mAdc}, V_{CE} = 20 \text{ Vdc}$ )	BF420 BF422	h <sub>FE</sub>	50 50	- -	-
Collector – Emitter Saturation Voltage (I <sub>C</sub> = 20 mAdc, I <sub>B</sub> = 2.0 mAdc)		V <sub>CE(sat)</sub>	-	0.5	Vdc
Base – Emitter Saturation Voltage $(I_C = 20 \text{ mAdc}, I_B = 2.0 \text{ mAdc})$		V <sub>BE(sat)</sub>	_	2.0	Vdc
SMALL-SIGNAL CHARACTERISTICS		•	•	•	•
Current Gain – Bandwidth Product (I <sub>C</sub> = 10 mAdc, V <sub>CE</sub> = 10 Vdc, f = 20 MHz)		f <sub>T</sub>	60	_	MHz
Common Emitter Feedback Capacitance (V <sub>CB</sub> = 30 Vdc, I <sub>E</sub> = 0, f = 1.0 MHz)		C <sub>re</sub>	_	1.6	pF

<sup>1.</sup> Pulse Test: Pulse Width  $\leq$  300  $\mu$ s; Duty Cycle  $\leq$  2.0%.

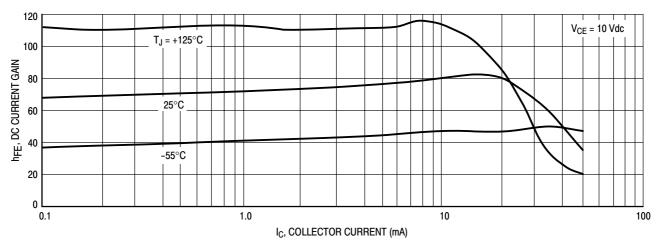


Figure 1. DC Current Gain

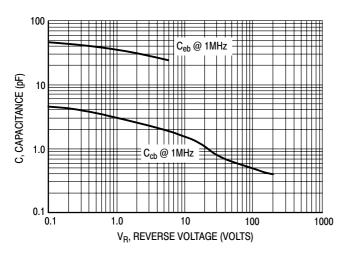


Figure 2. Capacitance

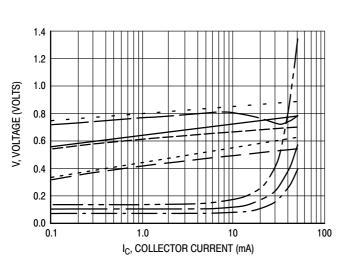


Figure 4. "ON" Voltages

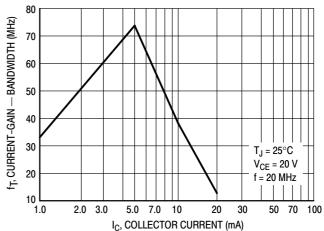
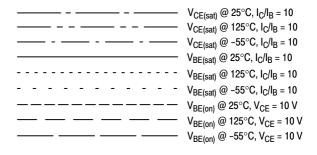
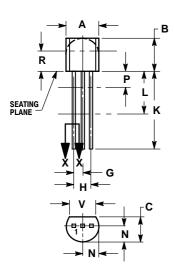


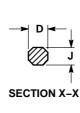
Figure 3. Current-Gain - Bandwidth



#### PACKAGE DIMENSIONS

**TO-92** (**TO-226**) CASE 029-11 ISSUE AL





#### NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI
   Y14 5M 1982
- Y14.5M, 1982.
  2. CONTROLLING DIMENSION: INCH.
- CONTOUR OF PACKAGE BEYOND DIMENSION R
   IS UNCONTROLLED.
- LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIM	ETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
С	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
Р		0.100		2.54
R	0.115		2.93	
V	0.135		3.43	

#### STYLE 14:

- PIN 1. EMITTER
  - 2. COLLECTOR
  - 3. BASE

ON Semiconductor and are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

#### **PUBLICATION ORDERING INFORMATION**

#### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 61312, Phoenix, Arizona 85082–1312 USA Phone: 480–829–7710 or 800–344–3860 Toll Free USA/Canada Fax: 480–829–7709 or 800–344–3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800–282–9855 Toll Free USA/Canada

Japan: ON Semiconductor, Japan Customer Focus Center 2–9–1 Kamimeguro, Meguro–ku, Tokyo, Japan 153–0051 Phone: 81–3–5773–3850

ON Semiconductor Website: http://onsemi.com

Order Literature: http://www.onsemi.com/litorder

For additional information, please contact your local Sales Representative.